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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:	Tsern et al.	Confirmation No.:	5522
Serial No.:	10/742,327	Art Unit:	3694
Filed:	December 18, 2003	Examiner:	Cheung, Mary Da Zhi Wang
For:	<i>Power Control System for Synchronous Memory Device</i>	Attorney Docket No.:	60809-0146-US

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure provisions of 37 C.F.R. §1.56, there is hereby provided certain information which the Examiner may consider material to the examination of the subject U.S. patent application. It is requested that the Examiner make this information of record if it is deemed material to the examination of the application.

1. Enclosures accompanying this Information Disclosure Statement are:
 - 1a. A list of all patents, publications, applications, or other information submitted for consideration by the office.
 - 1b. A legible copy of:
 - Each U.S. patent application publication and U.S. and foreign patent;
 - Each publication or that portion which caused it to be listed on the PTO-1449;
 - For each cited pending U.S. application, the application specification including the claims, and any drawing of the application, or portion of the application which caused it to be listed on the PTO-1449 including any claims directed to that portion;
 - ~~any~~ other information or portion which caused it to be listed on the PTO-1449.
2. This Information Disclosure Statement is filed under 37 C.F.R. §1.97(b):
 - Within three months of the filing date of a national application other than a continued prosecution application under §1.53(d);

- Within three months of the date of entry of the national stage as set forth in §1.491 in an international application;
- Before the mailing of the first Office action on the merits;
- Before the mailing of a first Office action after the filing of a request for continued examination under §1.114.

3. This Information Disclosure Statement is filed under 37 C.F.R. §1.97(c) after the period specified in 37 C.F.R. §1.97(b), but before the mailing date of any of a final action under 37 C.F.R. §1.113, a notice of allowance under 37 C.F.R. §1.311 or an action that otherwise closes prosecution in the application.

(Check either Item 3a or 3b)

3a. The Certification Statement in Item 5 below is applicable. Accordingly, no fee is required.

3b. The \$180.00 fee set forth in 37 C.F.R. §1.17(p) in accordance with 37 C.F.R. §1.97(c) is:
 enclosed
 to be charged to Morgan, Lewis & Bockius LLP Deposit Account No. 50-0310 (order no.).

(Item 3b to be checked if any reference known for more than 3 months)

4. This Information Disclosure Statement is filed under 37 C.F.R. §1.97(d) after the period specified in 37 C.F.R. §1.97(c), but on or before the date of payment of the issue fee.

(Check either Item 4a or 4b)

4a. The Certification Statement in Item 5 below is applicable.

4.b. The \$180.00 fee set forth in 37 C.F.R. §1.17(p) is:
 enclosed
 to be charged to Morgan, Lewis & Bockius LLP Deposit Account No. 50-0310 (order no. 60809-0146US).

5. Certification Statement (applicable if Item 3a or Item 4a is checked)

(Check either Item 5a, 5b or 5c)

5a. In accordance with 37 C.F.R. §1.97(e)(1), it is certified that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

5b. In accordance with 37 C.F.R. §1.97(e)(2), it is certified that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the information disclosure statement.

5c. Pursuant to 37 C.F.R. §1.704(d), each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart application, and the communication was not received by any individual designated in 37 C.F.R. §1.56(c) more than thirty days prior to the filing of this information disclosure statement.

6. Copies of each cited U.S. patent and each U.S. patent application publication are not enclosed pursuant to the USPTO OG Notice dated 05 August 2003 waiving the requirement under 37 C.F.R. 1.98(a)(2)(i) for U.S. patent applications filed after June 30, 2003.

7. This application is a continuation application under 37 C.F.R. §1.53(b) or (d).

(Check appropriate Items 7a, 7b and/or 7c)

7a. A Petition to Withdraw from issue under 37 C.F.R. §1.313(b)(5) is concurrently filed herewith.

7b. Copies of publications listed on Form PTO-1449 from prior application Serial No. _____, filed on _____, of which this application claims priority under 35 U.S.C. §120, are not being submitted pursuant to 37 C.F.R. §1.98(d).

7c. Copies of the publications listed on Form PTO-1449 were not previously cited in prior application Serial No. _____, filed on _____, and are provided herewith.

8. This is a Supplemental Information Disclosure Statement. (Check Item 8a)

8a. This Supplemental Information Disclosure Statement under 37 C.F.R. §1.97(f) supplements the Information Disclosure Statement filed on _____. A bona fide attempt was made to comply with 37 C.F.R. §1.98, but inadvertent omissions were made. These omissions have been corrected herein. Accordingly, additional time is requested so that this Supplemental Information Disclosure Statement can be considered as if properly filed on _____.

9. In accordance with 37 C.F.R. §1.98, a concise explanation of what is presently understood to be the relevance of each non-English language publication is:

(Check Item 9a, 9b, or 9c)

9a. satisfied because all non-English language publications were cited on the enclosed English language copy of the PCT International Search Report or the search report from a counterpart foreign application indicating the degree of relevance found by the foreign office.

9b. set forth in the application.

9c. enclosed as an attachment hereto.

10. The Commissioner is authorized to charge any additional fee required or credit any overpayment for this Information Disclosure Statement and/or Petition to Morgan, Lewis & Bockius LLP Deposit Account No. 50-0310 (order no. 60809-0146-US).

11. No admission is made that the information cited in this Statement is, or is considered to be, material to patentability nor a representation that a search has been made (other than a search report of a foreign counterpart application or PCT International Search Report if submitted herewith). 37 C.F.R. §§1.97(g) and (h).

Respectfully submitted,

Date: July 5, 2007

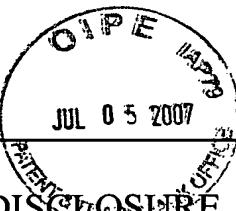


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INFORMATION DISCLOSURE

CITATION

PTO-1449

Complete If Known

Application Number	10/742,327
Filing Date	December 18, 2003
First Named Inventor	Tsern et al.
Art Unit	3694
Examiner Name	Cheung, Mary Da Zhi Wang

Sheet 1 of 3 Attorney Docket Number 60809-0146-US

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number Number - Kind Code ¹	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass	Filing Date if Appropriate
		5,268,639	07/07/1993	Gasbarro			
		5,335,206	08/02/1994	Kawamoto			
		5,359,722	10/25/1994	Chan			
		5,384,737	01/24/1995	Childs et al.	365	189.05	
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		5,917,760	06/29/1999	Millar			

Examiner Signature	1-PA/3645538.1	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ² Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ³ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁴ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁵ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

INFORMATION DISCLOSURE CITATION PTO-1449				<i>Complete If Known</i>		
Sheet	2	of	3	Application Number	10/742,327	
				Filing Date	December 18, 2003	
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				Art Unit	3694	
				Examiner Name	Cheung, Mary Da Zhi Wang	
				Attorney Docket Number	60809-0146-US	

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FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Sub-class	Translation	
		Country Code ² - Number ³ - Kind Code ⁴ (if known)					Yes	No

OTHER NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
		BURSKY, "Memory-CPU Interface Speeds Up Data Transfers"
		CHOI, "16Mbit Synchronous DRAM with 125Mbyte/sec Data Rate".
		GARY et al., "The PowerPC™ 603 Microprocessor: A Low-Power Design for Portable Applications"
		GILLINGHAM, "SLDRAM Architectural and Functional Overview," SDRAM Consortium, August 29, 1997.
		HAN, "Skew Minimization Techniques for 256M-bit Synchronous DRAM and beyond," 1996 Symposium on VLSI Circuits Digest of Technical Papers.
		HOROWITZ, "PLL Design for a 500 MB/s Interface," 1993 IEEE International Solid-State Circuits Conference, Digest of Technical Papers, February 25, 1993.
		KODAMA, "A 150-MHz 4-Bank 64M-bit SDRAM with Address Incrementing Pipeline Scheme," 1994 Symposium on VLSI Circuits Digest of Technical Papers, 1994
		KUSHIYAMA, "A 500-Megabyte/s Data-Rate 4.5M DRAM," IEEE Journal of Solid-State Circuits, Vol. 28 No. 4, April 1993
		KUSHIYAMA, "500Mbyte/sec Data-Rate 512 Kbits x 9 DRAM Using a Novel I/O Interface," 1992 Symposium on VLSI Circuits Digest of Technical Papers, 1992
		LEE et al., "A 2.5 V CMOS Delay-Locked Loop for an 18 Mbit, 500 Megabyte/s DRAM"

Examiner Signature	1-PA/3645538.1	Date Considered
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	Rambus, Inc., "Rambus Interface Technical Description" January 20, 1992
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	Rambus Inc, 8/9-Mbit (1M x 8/9) and 16/18 (2M x 8/9) RDRAM Data Sheet, March 1996.
	Rambus Inc, 16/18Mbit (2Mx8/9) & 64/72Mbit (8Mx8/9) Concurrent RDRAM Data Sheet, July 1996.
	SLDRAM Consortium Data Sheet, "400 Mb/s/pin SLDRAM Advance Data Sheet," August 27, 1997.
	SyncLink Consortium, "SyncLink Feasibility v1.1," April 30, 1996.

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